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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/755,387	01/13/2004	Hidekazu Kawashima	NEC03P207-SYa	9950	
21254	7590 09/28/2005		EXAMINER		
MCGINN INTELLECTUAL PROPERTY LAW GROUP, PLLC			GUERRERO, MARIA F		
8321 OLD CO SUITE 200	OURTHOUSE ROAD	ART UNIT	PAPER NUMBER		
	A 22182-3817	2822			

DATE MAILED: 09/28/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

		Applicati	on No.	Applicant(s)				
Office Action Summary		10/755,3	87	KAWASHIMA ET	KAWASHIMA ET AL.			
		Examine	r	Art Unit				
		Maria Gu	errero	2822				
	The MAILING DATE of this communication	on appears on th	e cover sheet wi	th the correspondence ac	ddress			
Period fo	•			MG				
WHIC - Exter after - If NO - Failu Any r	ORTENED STATUTORY PERIOD FOR INCHEMENT IS LONGER, FROM THE MAIL! Insigns of time may be available under the provisions of 37 sold (6) MONTHS from the mailing date of this communical period for reply is specified above, the maximum statutory re to reply within the set or extended period for reply will, by eply received by the Office later than three months after that patent term adjustment. See 37 CFR 1.704(b).	NG DATE OF TI CFR 1.136(a). In no ex- tion. period will apply and v y statute, cause the app	HIS COMMUNION AND AND AND AND AND AND AND AND AND AN	CATION. eply be timely filed ITHS from the mailing date of this of the company	· .			
Status				•				
1)🛛	Responsive to communication(s) filed on	27 April 2004						
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′=	· · ·	-		ers, prosecution as to the	e merits is			
تـــارت	3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.							
	on of Claims				•			
	4)⊠ Claim(s) <u>1-16</u> is/are pending in the application.							
	4a) Of the above claim(s) is/are withdrawn from consideration.							
	5) Claim(s) is/are allowed.							
•	Claim(s) is/are rejected.		•					
· ·	Claim(s) is/are objected to. Claim(s) <u>1-16</u> are subject to restriction ar	nd/or alastian ra	auiromont					
0)[Claim(s) 1-10 are subject to restriction ar	na/or election re	yuli ement.					
Applicati	on Papers				. •			
9)[] :	The specification is objected to by the Exa	aminer.	•					
10) 🔲 🤈	The drawing(s) filed on is/are: a)[accepted or b) ☐ objected to !	by the Examiner.				
	Applicant may not request that any objection	to the drawing(s)	be held in abeyan	ce. See 37 CFR 1.85(a).				
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).								
11)[The oath or declaration is objected to by t	the Examiner. N	ote the attached	I Office Action or form P	TO-152.			
Priority u	nder 35 U.S.C. § 119							
12) 🔲 ,	Acknowledgment is made of a claim for fo	oreign priority un	ider 35 U.S.C. §	119(a)-(d) or (f).				
a) ☐ All b) ☐ Some * c) ☐ None of:								
	1. Certified copies of the priority documents have been received.							
	2. Certified copies of the priority documents have been received in Application No							
3. Copies of the certified copies of the priority documents have been received in this National Stage								
	application from the International E	Bureau (PCT Ru	le 17.2(a)).					
* See the attached detailed Office action for a list of the certified copies not received.								
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	·							
Attachment	c(s)							
	e of References Cited (PTO-892)			Summary (PTO-413)				
	e of Draftsperson's Patent Drawing Review (PTO-9- nation Disclosure Statement(s) (PTO-1449 or PTO/			s)/Mail Date formal Patent Application (PT)	O-152)			
Paper No(s)/Mail Date 6) Other:								

DETAILED ACTION

Election/Restrictions

Restriction to one of the following inventions is required under 35 U.S.C. 121:

- I. Claims 1-6, drawn to a method of producing layout pattern, classified in class 395, subclass 500.
- II. Claims 7-12, drawn to a device of producing a layout pattern, classified in class 716, subclass 18.
- III. Claims 13-16, drawn to a method of fabricating a semiconductor device, classified in class 438, subclass 453.

The inventions are distinct, each from the other because of the following reasons:

Inventions II and I are related as combination and subcombination. Inventions in this relationship are distinct if it can be shown that (1) the combination as claimed does not require the particulars of the subcombination as claimed for patentability, and (2) that the subcombination has utility by itself or in other combinations (MPEP § 806.05(c)). In the instant case, the combination as claimed does not require the particulars of the subcombination as claimed because the primitive cells can be arranged without the use of the netlist storage unit. The subcombination has separate utility such as the device can be used to producing a layout pattern on a device other than semiconductor.

Inventions III and I are related as combination and subcombination. Inventions in this relationship are distinct if it can be shown that (1) the combination as claimed does

Art Unit: 2822

not require the particulars of the subcombination as claimed for patentability, and (2) that the subcombination has utility by itself or in other combinations (MPEP § 806.05(c)). In the instant case, the combination as claimed does not require the particulars of the subcombination as claimed because the primitive cells can be arranged without the use of the netlist storage unit. The subcombination has separate utility such as the device can be used to producing a layout pattern on a device other than semiconductor.

Inventions II and III are related as combination and subcombination. Inventions in this relationship are distinct if it can be shown that (1) the combination as claimed does not require the particulars of the subcombination as claimed for patentability, and (2) that the subcombination has utility by itself or in other combinations (MPEP § 806.05(c)). In the instant case, the combination as claimed does not require the particulars of the subcombination as claimed because the method of producing the layout does not require the formation of trenches. The subcombination has separate utility such as fabricating a semiconductor device having planarized isolation regions.

Because these inventions are distinct for the reasons given above and have acquired a separate status in the art as shown by their different classification, restriction for examination purposes as indicated is proper.

Applicant is advised that the reply to this requirement to be complete must include an election of the invention to be examined even though the requirement be traversed (37 CFR 1.143).

Applicant is reminded that upon the cancellation of claims to a non-elected invention, the inventorship must be amended in compliance with 37 CFR 1.48(b) if one or more of the currently named inventors is no longer an inventor of at least one claim remaining in the application. Any amendment of inventorship must be accompanied by a request under 37 CFR 1.48(b) and by the fee required under 37 CFR 1.17(i).

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Maria Guerrero whose telephone number is 571-272-1837. The examiner can normally be reached on M-F (8:30-5:00).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Amir Zarabian can be reached on 571-272-1852. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

September 22, 2005

MARIA F. GUERRERO PRIMARY EXAMINER